

ELECTRONICALLY FILED

PATENT

Attorney Docket No. 1008-743.301

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application)	<u>PATENT APPLICATION</u>
)	
Inventor(s): Donald Paul Richmond II)	
)	
Application No.: 10/718,825)	
)	
Filed: November 11, 2003)	
)	
Title: Wafer Level Burn-In and Electrical Test System and Method)	
)	

Art Unit: 2829

Examiner: Arleen M. Vazquez

Confirmation No.: 1336

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

In response to the Office Action mailed September 15, 2006, please amend the above-identified application as follows:

Listing of Claims begins on page 2 of this paper.

Remarks/Arguments begin on page 11 of this paper.